

Nº Proceso	
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EMPRESA	SEPSAMEDHA, SLU	Fecha	February 2022
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Puesto Presupuestado	Si <input checked="" type="checkbox"/>	No <input type="checkbox"/>
Sustitución de (Nombre empleado)	Not Applicable	
Otra Justificación de Apertura	Capacity in Verification & Validation (V&V) Engineering, as required for development of systems.	
Responsable Inmediato		
Departamento:	Systems-EBU	Sección Sys-V&V
Nº de Puestos a Cubrir	1	

Puesto:	Verification & Validation Engineer
Formación Imprescindible	Engineering -degree- (Electronics, Telecommunications, Software) or (Mathematics, Physics) degree
Formación Valorable	Engineering -master- (Electronics, Telecommunications, Software)
Experiencia necesaria	At least 2 years of professional experience as V&V Engineer, designing and performing functional tests of embedded systems in the Railways and/or Aeronautics/Space and/or Automotive application domain(s).

Descripción del Puesto	
Misión	Verification and validation of systems developed/modified by Systems-EBU, mainly for onboard solutions and mostly for safety-related ones (Automatic Train Protection)
Reporta a	Head of Systems-Verification & Validation Area
Relaciones internas	Member of Systems-EBU team
Relaciones externas	End-User/Customer and External Services for Certification & Assessment of Systems
Funciones principales	<p>Specification and design of System Tests and also of Test Benches/Setups.</p> <p>Performance and reporting of functional system tests</p>
Conocimientos Técnicos	<p>Significant experience in preparation of Test Plans, Test Specs/Protocols, and high-level design and setup of Test Benches.</p> <p>Analysis of System Requirements Specs, so as to create comprehensive and practical System Tests Specs and protocols, suitable for a full validation of compliance of system with requirements, and aimed at finding performance limits and defects of systems under test.</p> <p>Experience in performing reviews of technical deliverables created in system development life-cycle, so as to detect/identify issues, lack of information, inconsistencies among them, especially for requirements specs, architectural design descriptions, interfaces specs and test protocols.</p>

Used to apply testing methods and techniques for validation of complex embedded and distributed systems.

Good knowledge and practice with Requirements and Tests management tools [DOORS, Visure, Test Link Quality Center) and defects-tracking tools,

Knowledge of MS-Windows and Linux OS at user-level.

Experience in tests scripting languages is highly desired.

Background in datacomms solutions (serial links RS232/485, IP networking, other).

Desirable knowledge of CENELEC Stds EN-50126/50128/50129, models of life-cycle of safety-related systems.

**Otros Requisitos**

Good level of written and spoken English

<b>Tipo de Contrato</b>	Permanent
<b>Tiempo estimado de la Obra (*)</b>	Not Applicable
<b>Categoría</b>	Engineer
<b>Banda Salarial Aprox.</b>	
<b>Dirección del C. de Trabajo</b>	Pinto
<b>Horario de Trabajo</b>	40 h/week, ordinary timetable

(\*) Rellenar únicamente en casos de Contratos Temporales de Obra